L Number	Hits	Search Text	DB	Time stamp
2	752	shift adj register with serial with parallel with conversion	USPAT	2003/11/21 09:25
3	0	shift adj register with serial adj to adj parallel adj conversion	USPAT	2003/11/21 09:26
4	326	shift adj register with serial$4parallel adj conversion	USPAT	2003/11/21 09:32
5	23	shift adj register with parallel$4series adj conversion	USPAT	2003/11/21 09:37
6	7	shift adj register with (parallel$4series and series$4parallel) adj conversion	USPAT	2003/11/21 09:42
7	0	preshift adj register with error adj (detection or correction)	USPAT	2003/11/21 09:43
8	294	shift adj register with error adj (detection or correction)	USPAT	2003/11/21 09:58
9	0	microprocessor with decod$4 with lighting adj device	USPAT	2003/11/21 09:59
10	4	microprocessor with decod$4 with lighting	USPAT	2003/11/21 10:07
11	1	partial adj shifting with shift adj register	USPAT	2003/11/21 10:04
12	3	preshift adj register	USPAT	2003/11/21 10:04
13	9	microprocessor with override with lighting	USPAT	2003/11/21 10:15
14	0	multiple adj load with shift adj register	USPAT	2003/11/21 10:16
15	3589	(partial or portion) with shift adj register	USPAT	2003/11/21 10:17
16	18	(partial or portion) with fill with shift adj register	USPAT	2003/11/21 10:35
17	3	successive with data with partial with shift adj register	USPAT	2003/11/21 10:33
18	3	preshift adj register	USPAT	2003/11/21 10:34
19	8	preshift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:35
20	20	(partial or portion) with fill with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:39
21	1245	data with portion with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:39
22	29	data with portion with shift adj register with error	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:43
23	43	hold adj register with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:49
24	414	portion adj shift$4 with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:50
25	2	portion adj shift$4 with shift adj register with error	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:54

26	0	portion with error$free with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 10:54
27	30	error$free with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:07
28	597	partial with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:07
29	9	partial with shift adj register with (fill or load)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:10
30	0	multiple adj (load or fill) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:10
31	4	plural adj (load or fill) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:12
32	6948	partial with error (checking or detecting) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:13
33	0	partial with error adj (checking or detecting) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:13
34	144	error adj (checking or detecting) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:50
35	8	incremental with error with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:53
36	0	incremental adj (load or fill) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:53
37	0	partial adj (load or fill) with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:54
38	100	4$1bit with 8$1bit with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 11:57
39	3	4$1bit with 8$1bit with shift adj register with error	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 12:31

40	2	3671865.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 12:44
41	9	3671865.URPN.	USPAT	2003/11/21 12:32
42	11	shift adj register with data adj loss	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 12:55
43	0	microprocessor with (auxiliary or supplemental) adj hardware adj device	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 12:56
44	5	microprocessor with (auxiliary or supplemental) adj hardware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 12:56
45	11907	microprocessor with hardware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 12:57
46	59	microprocessor with separate adj hardware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:11
47	42479	arbitration ajd control with lighting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:11
48	1	arbitration adj control with lighting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:11
49	11	arbitration with lighting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:17
50	1	retransmission with lighting with command	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:21
51	545	error with retransmission adj request	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:22
52	0	error with retransmission adj request with lighting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:22
53	4	error with retransmission adj request with control adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:23
54	0	arq with lighting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:23

55	2	arq with light$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:25
56	3	multiple with (partial or portion) with shift adj register with (load or fill)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:28
57	2165	(data or signal) with portion with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:29
58	47	(data or signal) with portion with shift adj register with load	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:32
59	0	error adj check adj input with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 13:33
60	75	error adj check with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:34
61	0	shift adj register with smaller adj input adj buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:35
62	0	shift adj register with small$3 adj input adj buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:35
63	0	shift adj register with small$3 adj input adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:36
64	422	shift adj register with input adj buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:36
65	0	shift adj register with input adj buffer with (staged or staggered)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 14:36
66	12	shift adj register with input adj buffer with (partial or portion)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:12
67	31843	input adj buffer error with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:15
68	0	input adj buffer adj error with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:15

69	0	input adj stage adj error with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:15
70	87237	byte wih error adj checking with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:16
71	1	byte with error adj checking with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:32
72	0	(plural or multiple) adj partial adj loads with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:33
73	2	(plural or multiple) adj partial with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:36
74	113	377/70.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:43
75	0	plural adj partial adj load with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:44
76	0	partial adj load with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:44
77	4	plural adj load with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:45
78	0	incomplete adj load with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:45
79	0	incomplete adj fill with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:46
80	0	partial adj fill with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:57
81	811	input adj buffer with shift	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 15:58
82	422	input adj buffer with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 16:43

83	0	multiple adj byte adj shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 16:38
84	8	multiple adj byte with shift adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 16:38
85	139	315/292.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 16:43
86	493	315/292,294,316.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/11/21 16:44

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